

Notice of References Cited	Application/Control No. 10/629,051	Applicant(s)/Patent Under Reexamination GEBARA ET AL.	
	Examiner Gopal C. Ray	Art Unit 2111	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,901,458	05-2005	Vuong, Vinh T.	710/14
*	B	US-6,606,656	08-2003	Carvey et al.	709/220
*	C	US-6,055,582	04-2000	Pascarella et al.	710/14
*	D	US-5,603,044	02-1997	Annapareddy et al.	710/301
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"Design advances in PCB/backplane interconnects for the propagation of high speed Gb/s digital signals" by Gisin et al. (abstract only) Publication Date: 1-3 Oct 2003
	V	
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.